

**JST80IS-1200BW 80A TRIAC**

Rev.A.1.1

DESCRIPTION:

The JST80IS-1200BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST80IS-1200BW snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST80IS-1200BW provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package ITO-247 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	80	A
V_{DRM}/V_{RRM}	1200	

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	1200	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	1200	V
RMS on-state current ($T_c \leq 90^\circ\text{C}$)	$I_{T(RMS)}$	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	800	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		880	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	3200	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	100	A/s
Peak gate current ($t_p=20\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	10	A

Average gate power dissipation (T _j =125)	P _{G(AV)}	0.5	W
Peak gate power	P _{GM}	25	W
Peak pulse voltage (T _j =25			

FIG.7 Test circuit for inductive and resistive tEC

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ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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JST80IS-1200BW	1200	50	ITO-247(Ins)	25	Tube

Document Revision History

Date	Revision	Changes
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