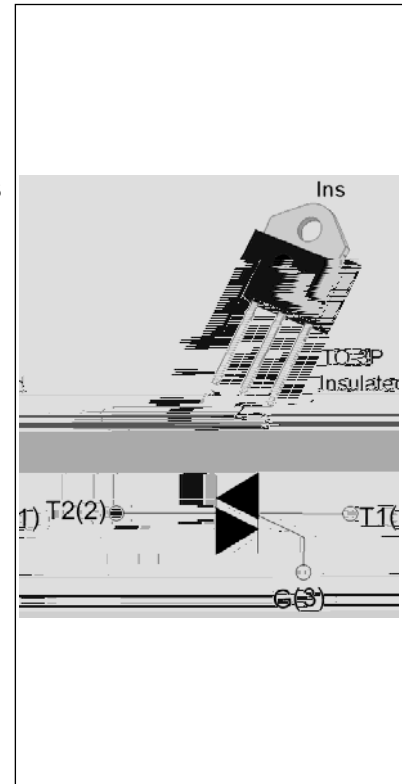




The JST26Z-1200CW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST26Z-1200CW snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST26Z-1200CW provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-3P is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	25	A
V_{DRM}/V_{RRM}	1200	V
$I_{GT} / /$	35/35/35	mA

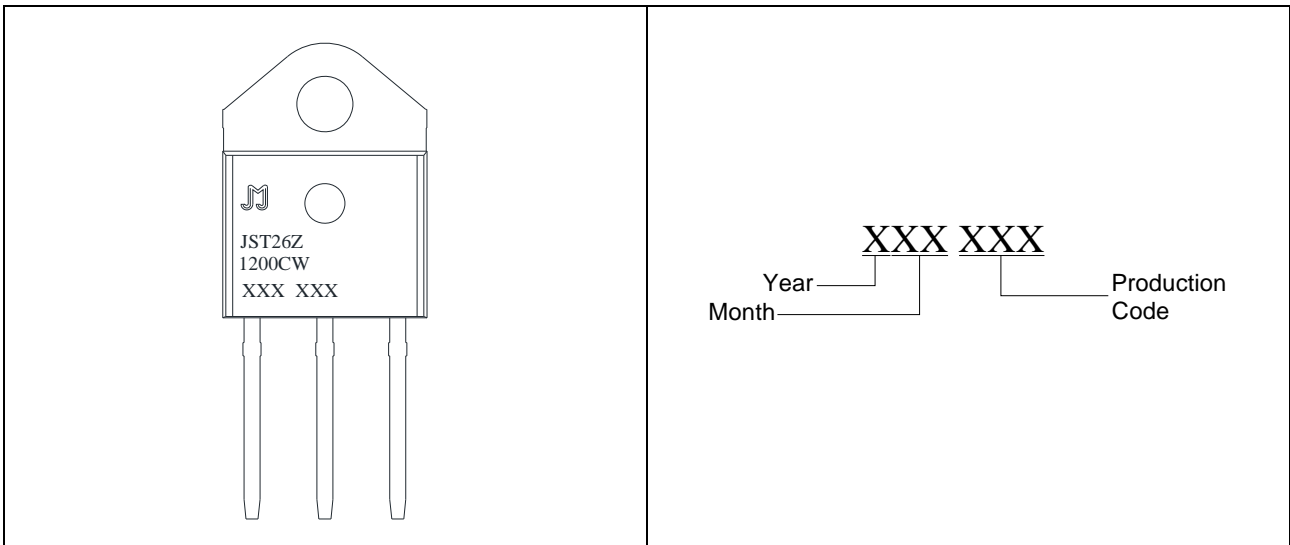
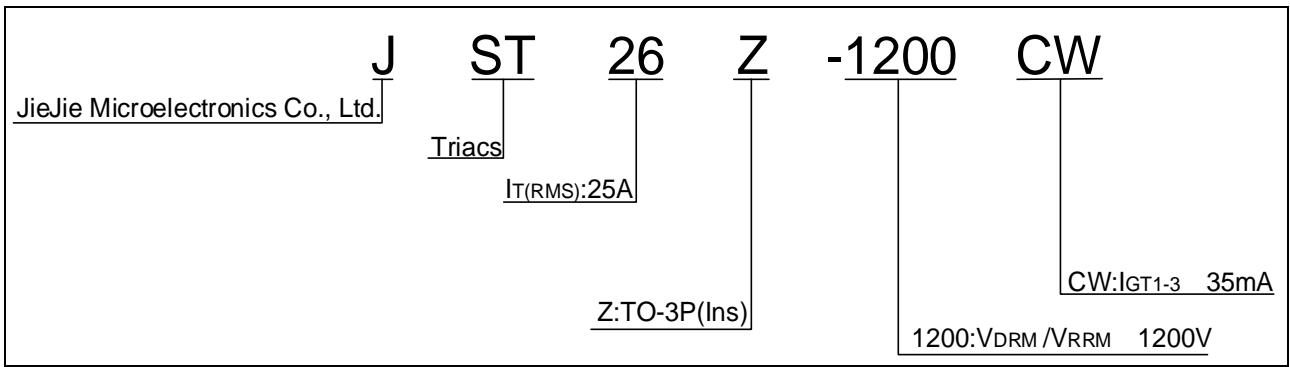
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	1200	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	1200	V
RMS on-state current ($T_c = 81^\circ\text{C}$)	$I_{T(RMS)}$	25	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	250	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		275	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	340	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	100	$\text{A}/\mu\text{s}$
Peak gate current ($t_p=20\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.5	W



Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25$; non-repetitive,off-state;FIG.7)	V_{pp}	2.5	kV

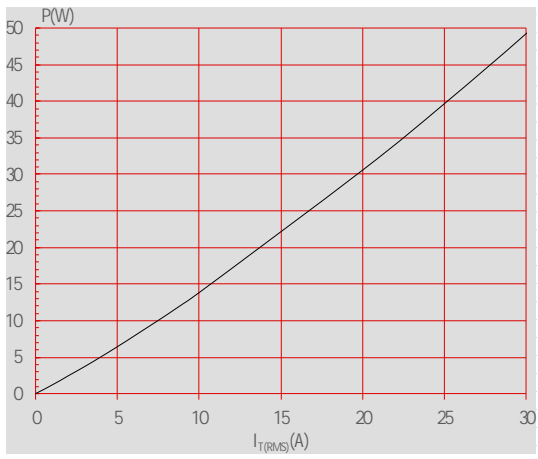
($T_j=25$ unless otherwise specified)

I_{GT}	$V_D=12V R_L=33$	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM} T_j=125$ $R_L=3.3k$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	70	mA
				80	
I_H	$I_T=500mA$		MAX.	60	mA
dV/dt	$V_D=800V$ Gate Open $T_j=125$		MIN.	1000	$V/\mu s$
$(dI/dt)_c$	$(dV/dt)_c=20V/\mu s T_j=125$		MIN.	15	A/ms
$t_{on} M$	$= = 0mF$				





Maximum power dissipation versus RMS on-state current



RMS on-state current versus case temperature

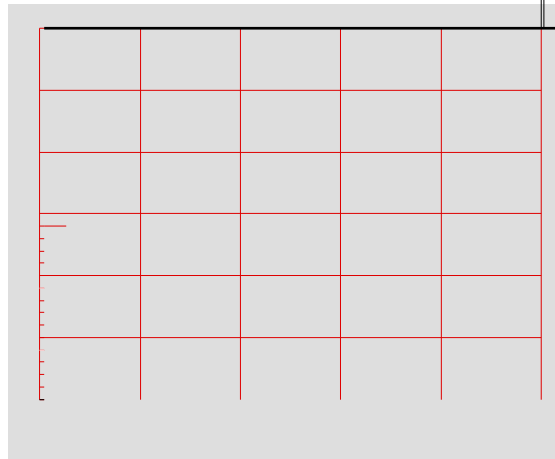
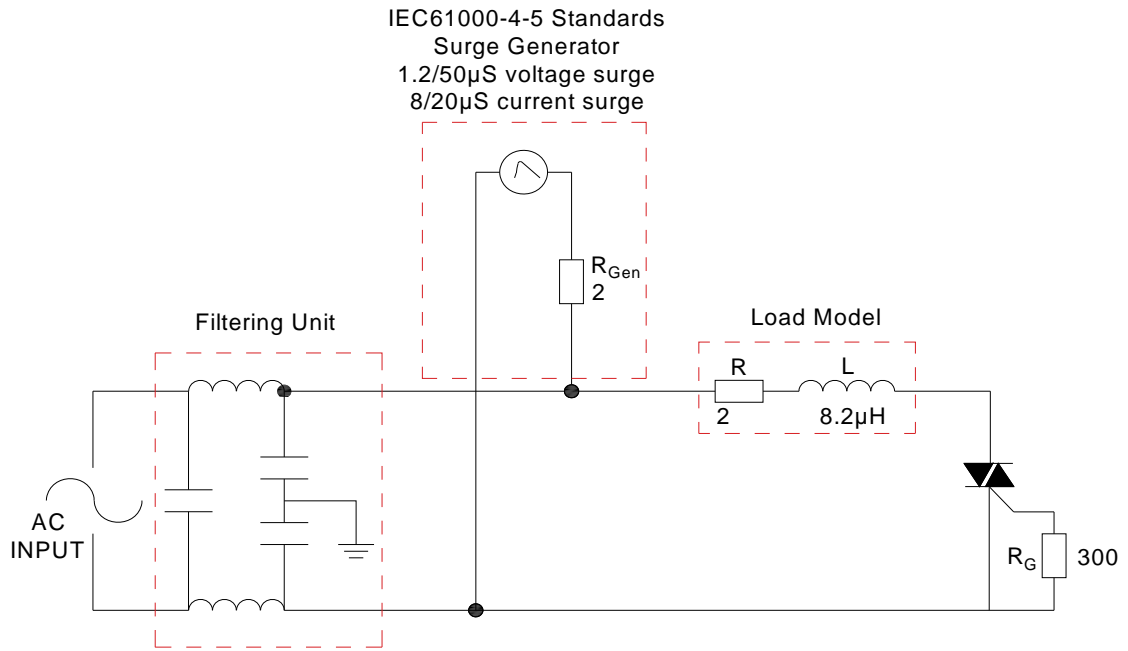




FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA



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