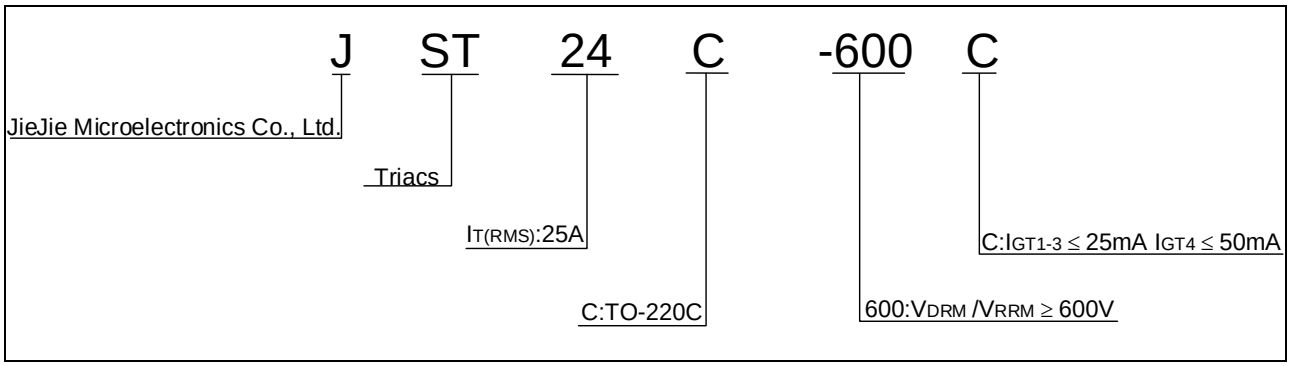
JST24C0

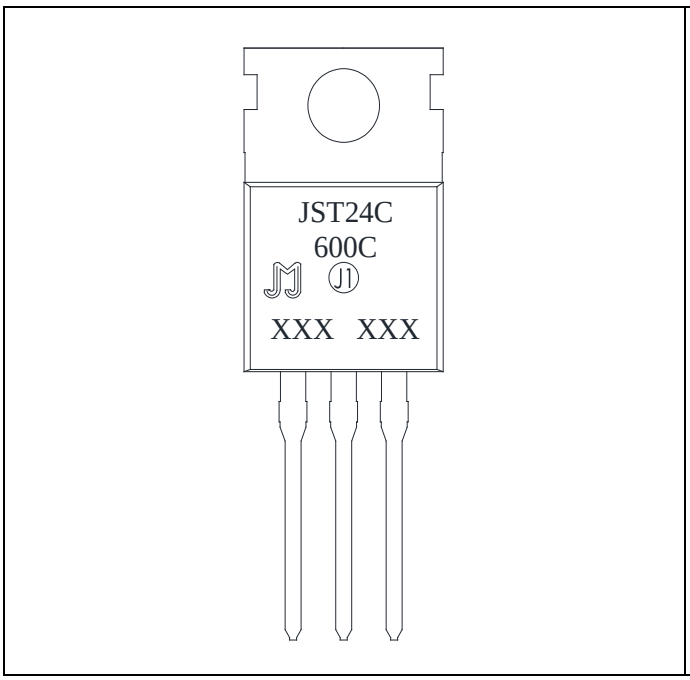
(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33Ω	- -	MAX.	25	mA
				50	
V_{GT}		ALL	MAX.	1	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	ALL	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	- -	MAX.	70	mA
				100	
I_H	I_T=500mA		MAX.	60	mA
dV/dt	V_D=400V Gate Open T_j=125		MIN.	800	V/μs
(dV/dt)c	(dI/dt)c=13.3A/ms, T_j=125		MIN.	6	V/μs
t_{on}	I_G=80mA I_A=400mA I_R=40mA T_j=25		TYP.	3	μs
t_{off}				50	

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM}=35A t_p=380μs	T_j=25	1.5	V
V_{TO}	Threshold voltage	T_j=125	0.75	V
R_D	Dynamic resistance	T_j=125	18	mΩ
I_{DRM}	V_D=V_{DRM} V_R=V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=125	1.5	mA

Symbol	Parameter	Value	Unit
$R_{th(j-}$			


J
ST
24
C
-600
C
JieJie Microelectronics Co., Ltd.
Triacs
IT(RMS):25A
C:TO-220C
600:VDRM /VRRM ≥ 600V
C:IGT1-3 ≤ 25mA IGT4 ≤ 50mA


JST24C
600C
J1
XXX XXX

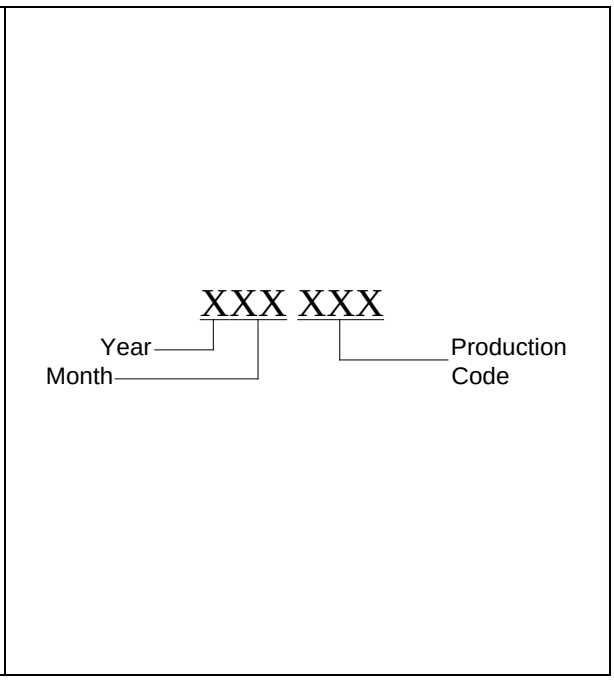

XXX XXX
Year
Month
Production
Code

FIG.1: Maximum power dissipation versus RMS on-state current

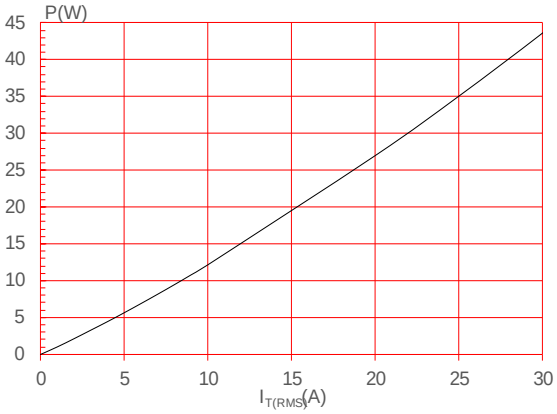


FIG.2: RMS on-state current versus case temperature

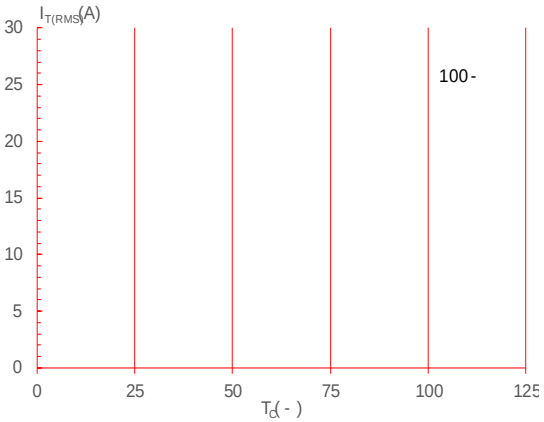


FIG.3: Surge peak on-state current versus number of cycles

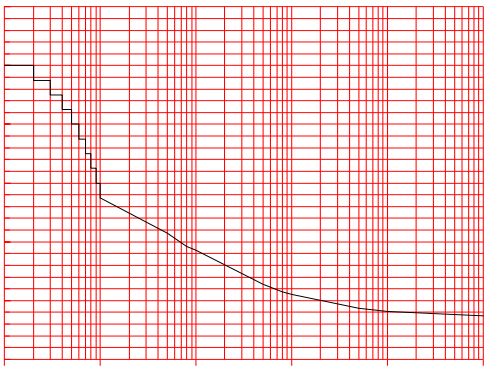


FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards ACAC

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST24C-600C						