T20F-1200CW 20A TRIAC

Rev.A.1.1

SCRIPTION:

e JST20F-1200CW triac is suitable for general purpose AC
tching. It can be used as an ON/OFF function in applications
ch as heating regulation, induction motor starting circuits, for
ase control operation in light dimmers, motor speed controllers.
T20F-1200CW snubberless triac is especially recommended for
e on inductive loads. By using an external plastic package,
T20F-1200CW provides a rated insulation voltage of 2000
MS, complying with UL standards (File ref: E252906).
ckage TO-220F is RoHS compliant.

AIN FEATURES

SOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
orage junction temperature range	T_{stg}	-40-150	
epetitive peak off-state voltage (T_j=25)	V_{DRM}	1200	V
epetitive peak reverse voltage (T_j=25)	V_{RRM}	1200	V

Peak gate power	P_{GM}	10	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	4	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D =12V R_L =33Ω	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	V_D =V_{DRM} T_j =125 R_L =3.3kΩ	- -	MIN.	0.2	V
I_L	I_G =1.2I_{GT}	-	MAX.	50	mA

ORDERING INFORMATION

J ST 20 F -1200 CW

- J: JieJie Microelectronics Co., Ltd.
- ST: Triacs
- 20: $I_{T(RMS)}$:20A
- F: F:TO-220F(Ins)
- -1200: 1200:V_{DRM} /V_{RRM} 1 1200V
- CW: CW:I_{GT1-3} 0 35mA

MARKING

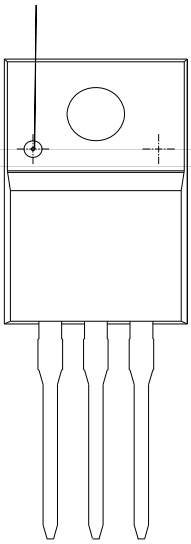


FIG.1: Maximum power dissipation versus RMS on-state current

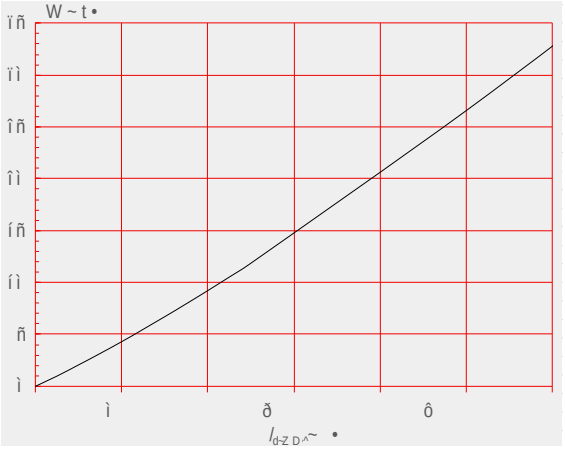


FIG.2: RMS on-state current versus case temperature

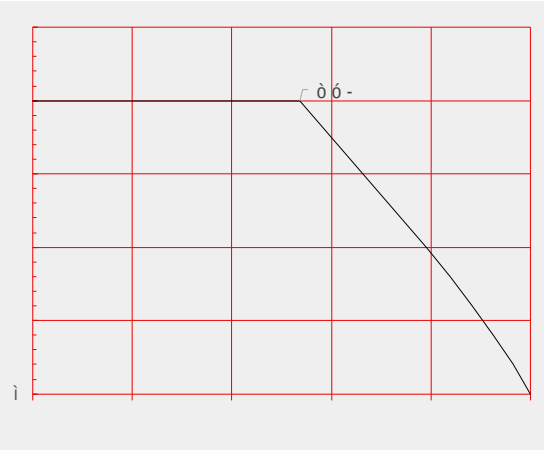
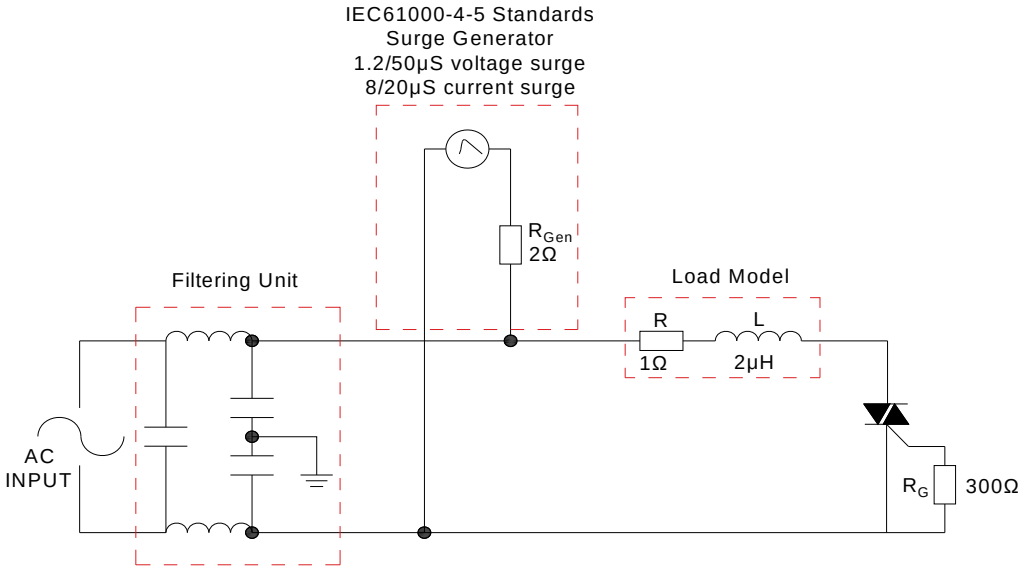
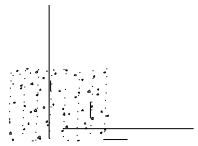


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



PACKAGE MECHANICAL DATA



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