

JIEJIE MICROELECTRONICS CO. , LTD.

Avere gate pow (6 (er))13 dissipatje1125)	P _{G(AV)}	0.5	W
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ORDERING INFORMATION

<u>J</u>	<u>ST</u>	<u>20</u>	<u>A</u>	<u>-800</u>	<u>BW</u>
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}:20A$			
		$A:TO-220A(Ins)$			$BW:I_{GT1-3} 050mA$
				$800:V_{DRM}/V_{RRM} 1800V$	

MARKING

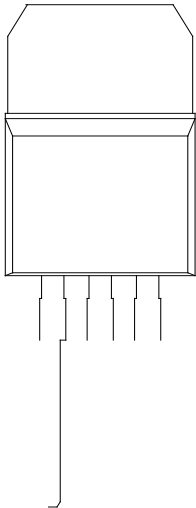


FIG.1: Maximum power dissipation versus RMS on-state current

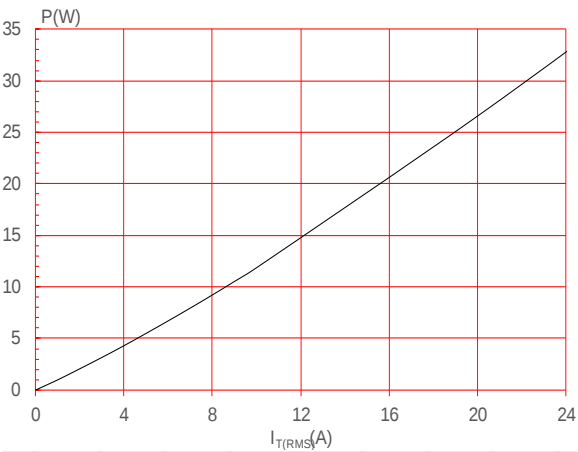


FIG.2: RMS on-state current versus case temperature

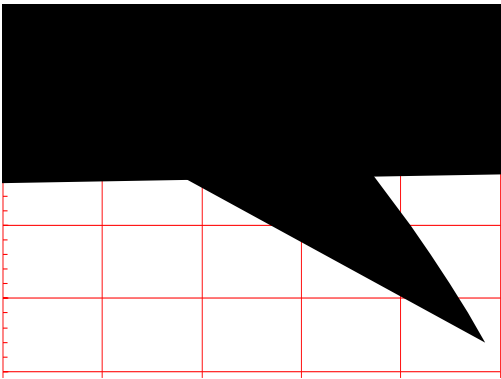
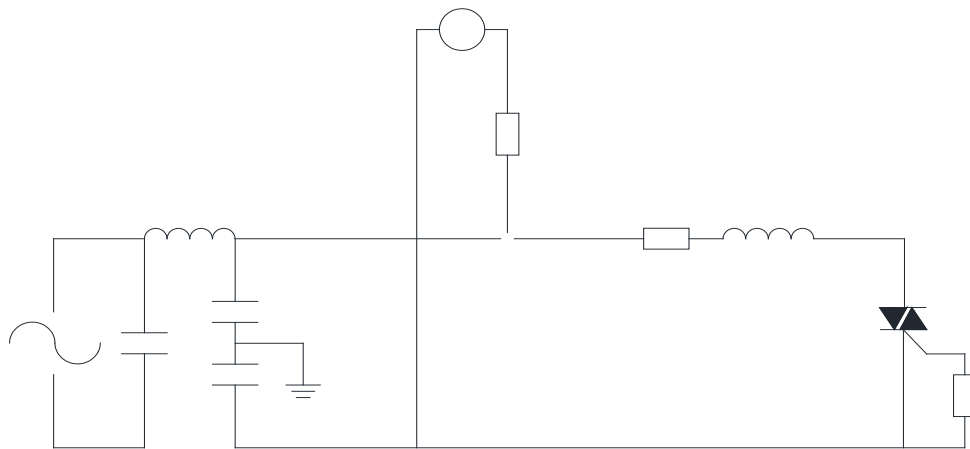


FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST20A-800BW	800	50	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.13, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

<http://www.jjwdz.com>