



Rev.A.1.1

<http://www.jjwdz.com>

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	3	kV
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ELECTRICAL CHARACTERISTICS (unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	5	mA
V _{GT}		- -	MAX.	1	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3k	- -	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	-	MAX.	15	mA
				20	
I _H	I _T =500mA		MAX.	15	mA
dV/dt	V _D =400V Gate Open T _j =125		MIN.	100	V s
(dI/dt) _c	(dV/dt) _c =1 9 V _j =125		MIN.	1.5	A/ms
t _{on}	I _G =10mA I _A =200mA I _R =20mA T _j =25		TYP.	3	s
t _{off}				25	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=22.5A$ $t_p=380$ s	$T_j=25$	1.5	V
V_{TO}	Threshold voltage	$T_j=125$	0.77	V
R_D	Dynamic resistance	$T_j=125$	30	P
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	A
I_{RRM}		$T_j=125$	0.4	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to ce (AC)	1.1	/W
$R_{th(j-a)}$	/W		

ORDERING INFORMATION

J	ST	16	C	-600	TW
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}:16A$	$C:TO-220C$	$600:V_{DRM}/V_{RRM} \geq 600V$	$TW:I_{GT1-3} \leq 5mA$

MARKING

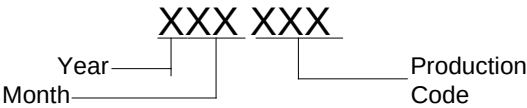
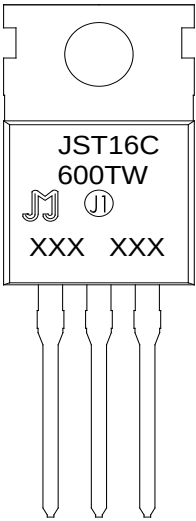
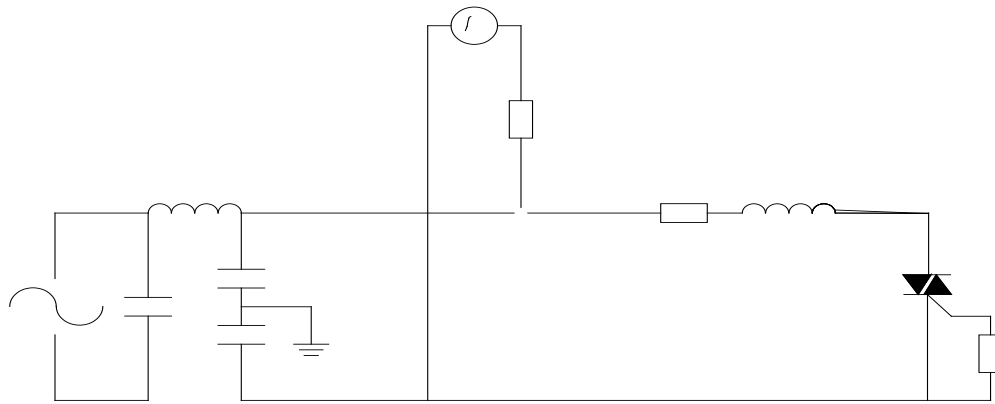


FIG.1: Maximum power dissipation versus RMS
on-state current

FIG.2:

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST16C-600TW	600	5	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

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