



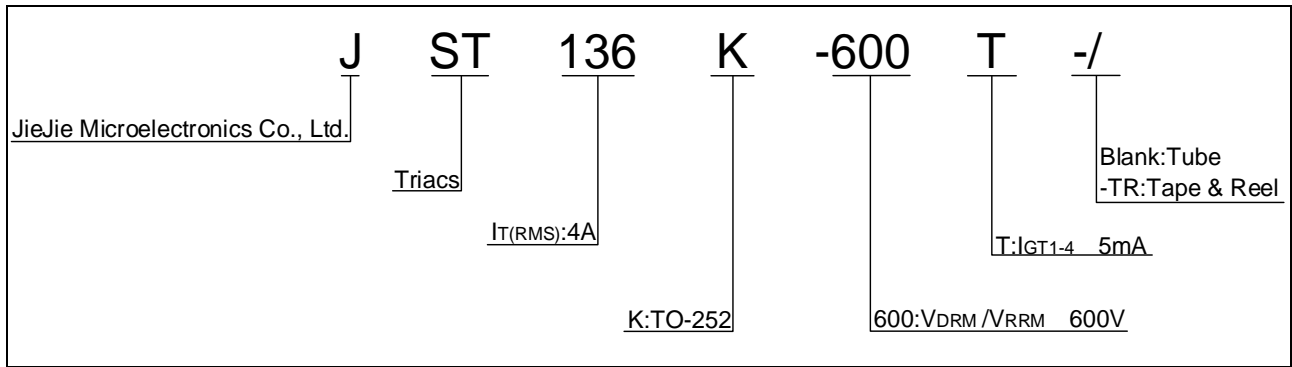
ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	ALL	MAX.	5	mA
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	10	mA
		-		15	
I_H	$I_T=100mA$		MAX.	5	mA
dV/dt	$V_D=400V$ Gate Open $T_j=110$		MIN.	30	V/ μs
(dV/dt) _c	(dI/dt) _c =1.8A/ms, $T_j=110$		MIN.	1	V/ μs
t_{on}	$I_G=10mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	1	μs
t_{off}				12	

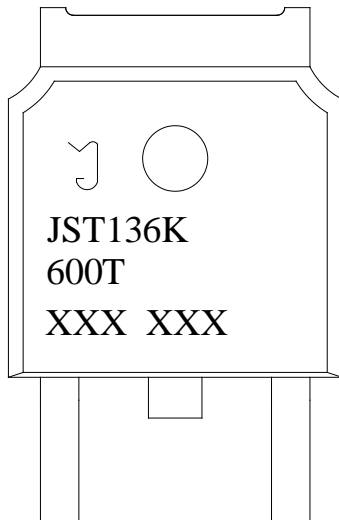
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5A$ $t_p=380\mu s$	$T_j=25$	1.7	V
V_{TO}	Threshold voltage	$T_j=125$	1.	

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MARKING



JST136K-600T

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

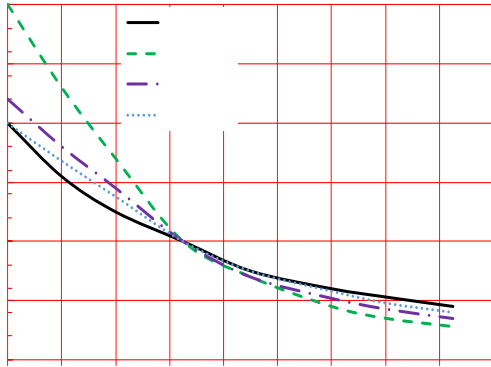


FIG.8 Test circuit for inductive and resistive loads to

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