

**JIEJIE MICROELECTRONICS CO., LTD.**

Peak pulse voltage ( $T_j=25$ ; non-repetitive,off-state;FIG.7)	$V_{PP}$	3.5	kV
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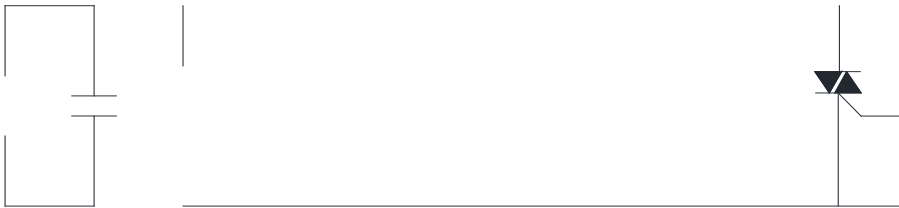
**ELECTRICAL CHARACTERISTICS** ( $T_j=25$  unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
$I_{GT}$	$V_D=12V R_L=33$	- -	MAX.	25	mA
				70	
$V_{GT}$	$V_D$	ALL	MAX.	1	V
$V_{GD}$					



**FIG.1:** Maximum power dissipation 39.6 W (u5) 11.6 W (u11.0) Tw Rpow 39.M04 3006 300.12 7-(M)4.5 W d [10

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





**PACKAGE MECHANICAL DATA**

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences L:/t. .