



JST134V-800DS 1A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST134V-800DS triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Package SOT-223 is RoHS compliant.

MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40-150	
Operating junction temperature range	T _j	-40-125	
Repetitive peak off-state voltage (T _j =25)	V _{DRM}	800	V
Repetitive peak reverse voltage (T _j =25)	V _{RRM}	800	V
RMS on-state current (T _c 0104)	I _{T(RMS)}	1	A

478 repetitive 20r2 62.64 294 0>>BDC44ref 537.1 re

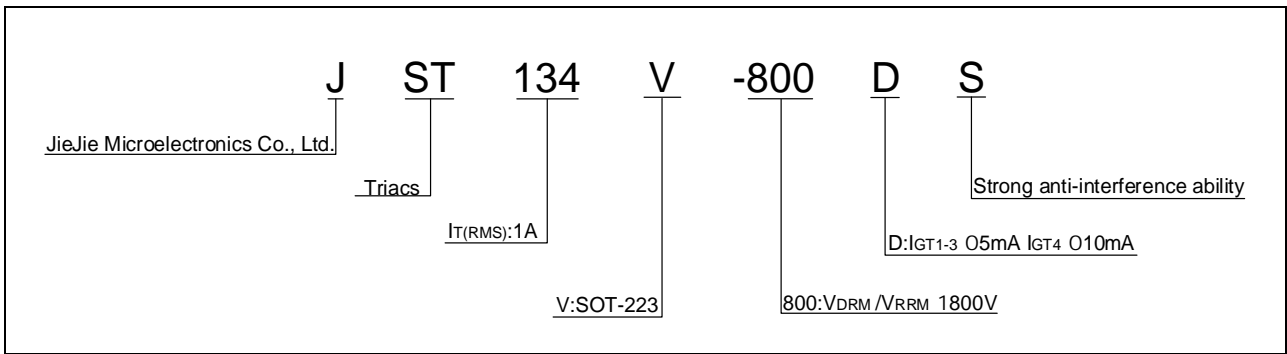
ELECTRICAL CHARACTERISTICS (unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	5	mA
				10	
V _{GT}		ALL	MAX.	1.3	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3k	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	- -	MAX.	10	mA
				20	
I _H	I _T =100mA		MAX.	7	mA
dV/dt	V _D =540V Gate Open T _j =110		MIN.	250	V s
(dV/dt) _c	(dI/dt) _c =1.8A/ms, T _j =110		MIN.	2.5	9 V

t_{on} I_G=20mA I_A=200mA I_R=20mA TYP. 2.5

T_j=25°C (unless otherwise specified) (T_j=125°C) (T_j=150°C) (T_j=175°C) (T_j=200°C) (T_j=225°C) (T_j=250°C) (T_j=275°C) (T_j=300°C) (T_j=325°C) (T_j=350°C) (T_j=375°C) (T_j=400°C) (T_j=425°C) (T_j=450°C) (T_j=475°C) (T_j=500°C) (T_j=525°C) (T_j=550°C) (T_j=575°C) (T_j=600°C) (T_j=625°C) (T_j=650°C) (T_j=675°C) (T_j=700°C) (T_j=725°C) (T_j=750°C) (T_j=775°C) (T_j=800°C) (T_j=825°C) (T_j=850°C) (T_j=875°C) (T_j=900°C) (T_j=925°C) (T_j=950°C) (T_j=975°C) (T_j=1000°C)

ORDERING INFORMATION



MARKING

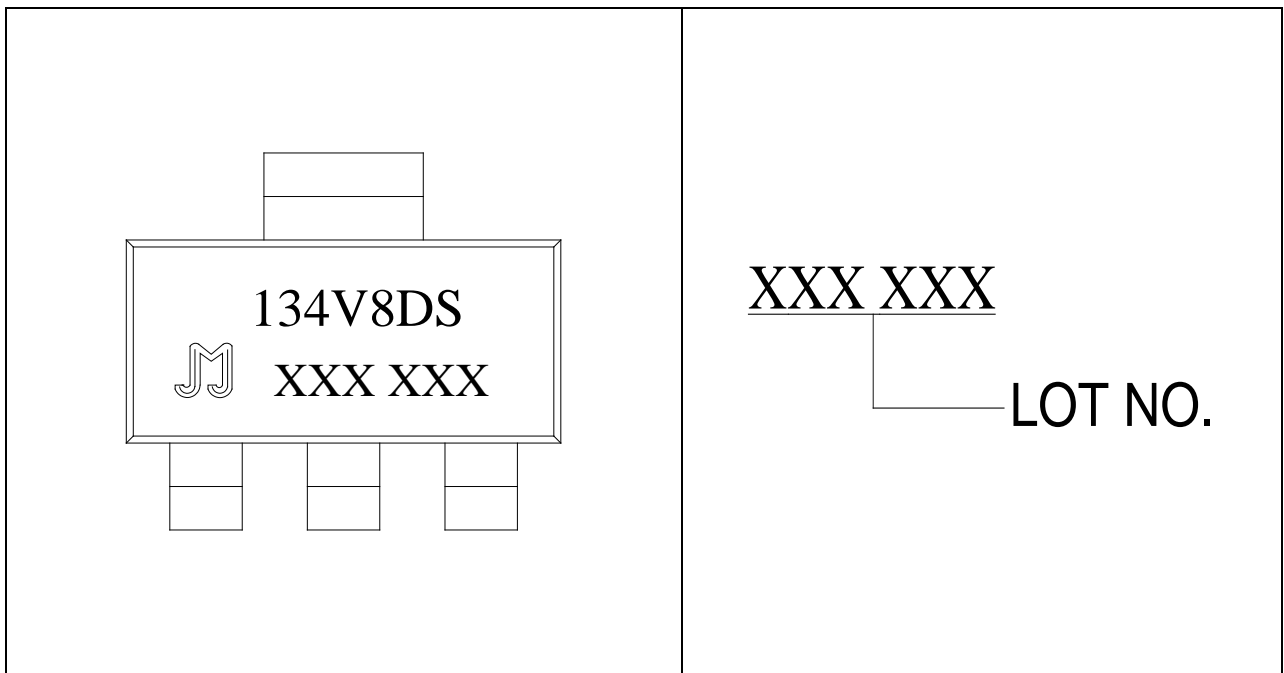


FIG.1: Maximum power dissipation versus RMS on-state current

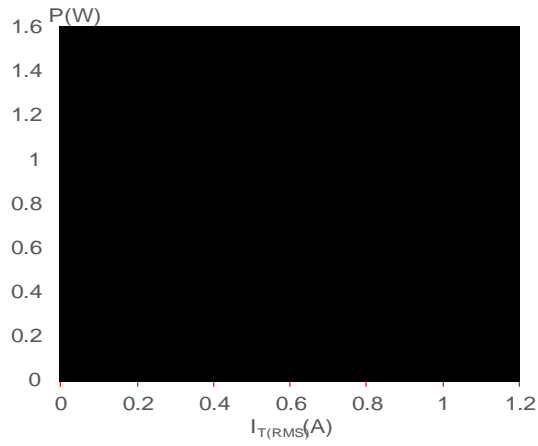


FIG.2: RMS on-state current versus case temperature

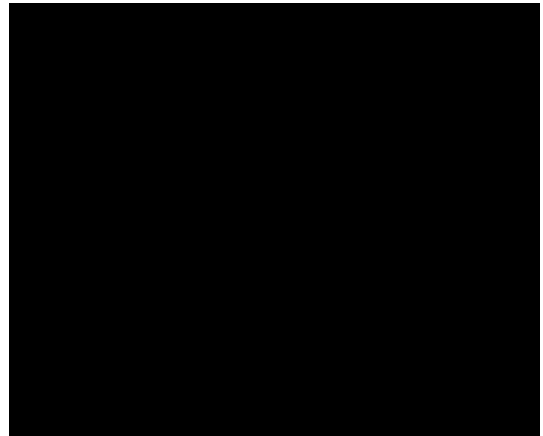


FIG.3: RMS on-state current versus ambient

temperature. The graph area is currently blank, indicating that the data for this figure is not visible or has been redacted.

FIG.8 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

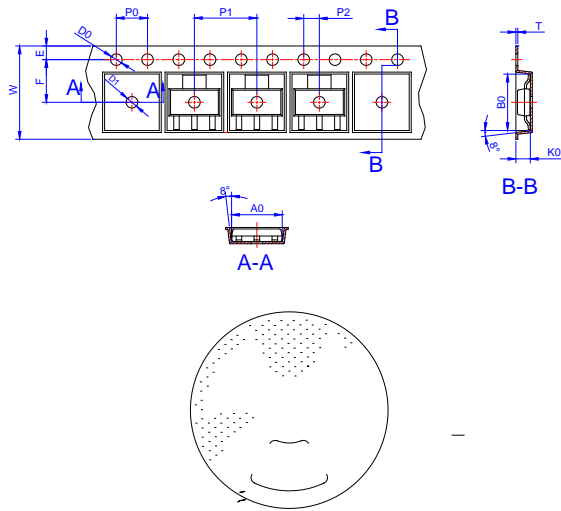
Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST134V-800DS	800	5	10	SOT-223	4,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Mar.25, 2025	A.1.0	Last update
Oct.24, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA

DELIVERY MODE



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	-	-	12.30	-	-	0.482
E	1.65	1.75	1.85	0.065	0.069	0.073
F	5.45	5.50	5.55	0.215	0.217	0.219
D0	1.50	1.55	1.60	0.059	0.061	0.063
D1	1.50	-	-	0.059	-	-
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.95	2.00	2.05	0.077	0.079	0.081
10P0	39.80	40.00	40.20	1.567	1.575	1.583
A0	6.85	6.95	7.05	0.269	0.273	0.276
B0	7.15	7.25	7.35	0.280	0.284	0.288
K0	1.95	2.05	2.15	0.076	0.080	0.084
T	0.20	0.25	0.30	0.008	0.010	0.012

