

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	4.5	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33\Omega$	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1	V

ORDERING INFORMATION

<u>J</u>	<u>ST</u>	<u>12</u>	<u>C</u>	<u>-1200</u>	<u>BW</u>
JieJie Microelectronics Co., Ltd.	Triacs				
	IT(RMS):12A				
		C:TO-220C			
					BW:IGT1-3 ≤ 50mA
					1200:VDRM /VRRM ≥ 1200V

MARKING

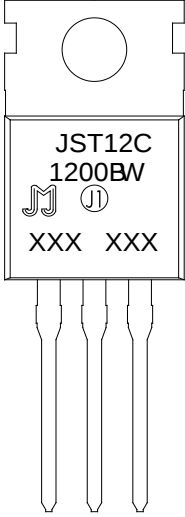
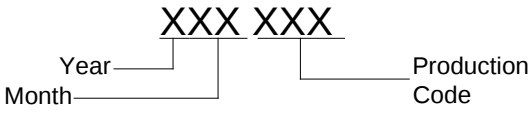
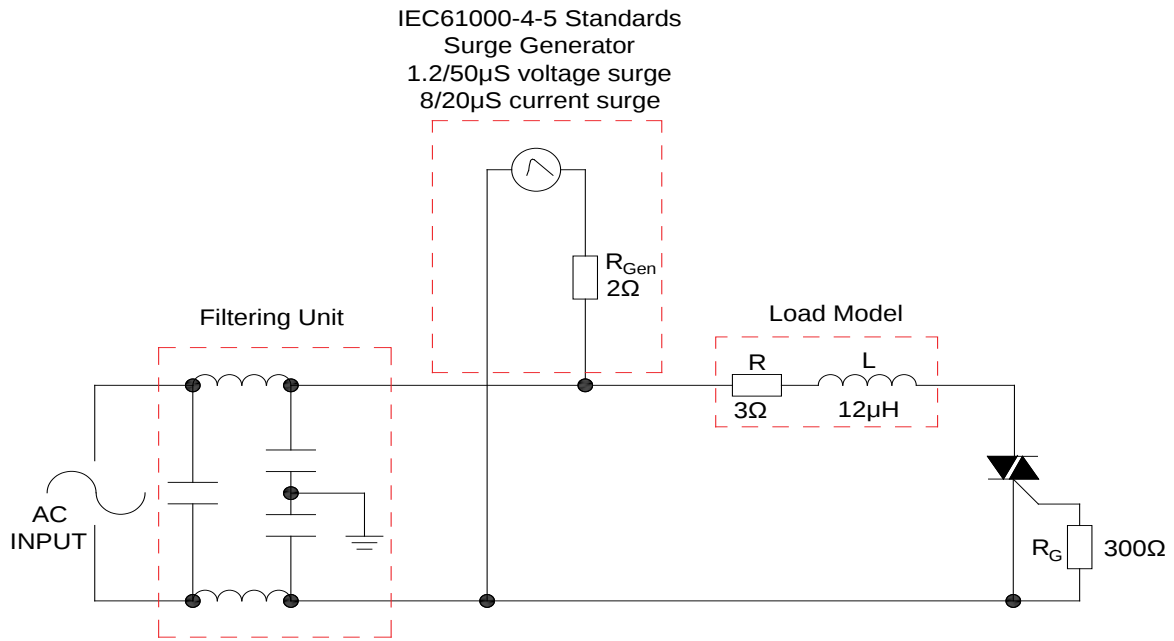
	
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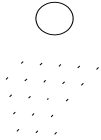
FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics

PACKAGE MECHANICAL DATA



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