



Co., Ltd.

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	J	ST	06	F	-800	SW
JieJie Microelectronics Co., Ltd.		Triacs	$I_{T(RMS)}: 6A$			
			F: TO-220F(Ins)			SW: $I_{GT1-3} \leq 10mA$
					$800: V_{DRM} / V_{RRM} \geq 800V$	

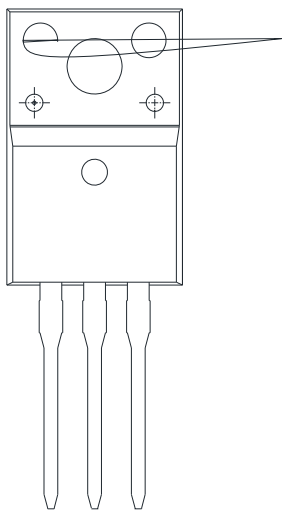


FIG.1: Maximum power dissipation versus RMS on-state current

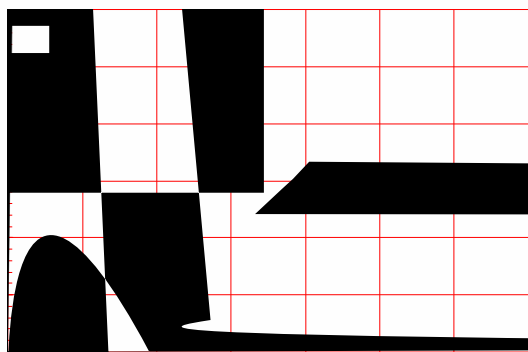
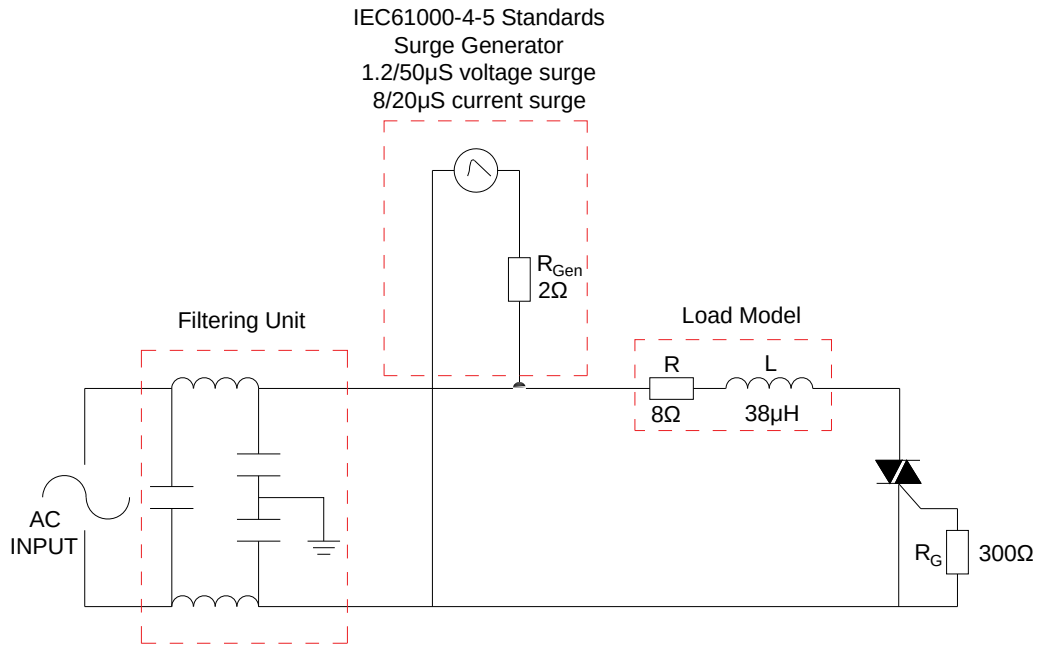


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST06F-800SW	800	10	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated
Oct.13, 2025	A.1.1	Revise PACKA > A' 0 a oA



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