

JST04V-600SW 1A TRIAC

Peak pulse voltage
($T_j=25^\circ\text{C}$; non-rept66Qiolvt)

ORDERING INFORMATION

<u>J</u>	<u>ST</u>	<u>04</u>	<u>V</u>	<u>-600</u>	<u>SW</u>
JieJie Microelectronics Co., Ltd.	Triacs				
	IT(RMS):1A				
		V:SOT-223			
				600:VDRM /VRRM 1600 V	SW:I _{GT1-3} 010 mA

MARKING

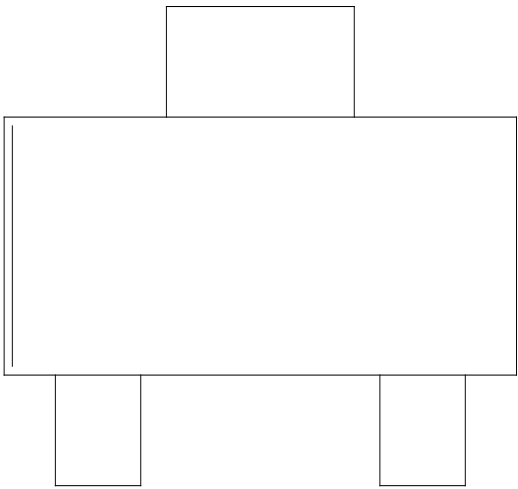


FIG.1: Maximum power dissipation versus RMS on-state current

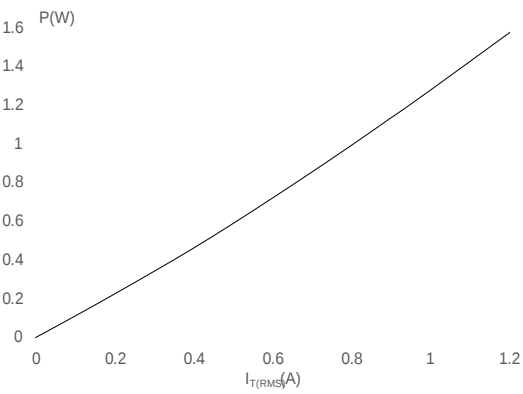


FIG.2: RMS on-state current versus case temperature

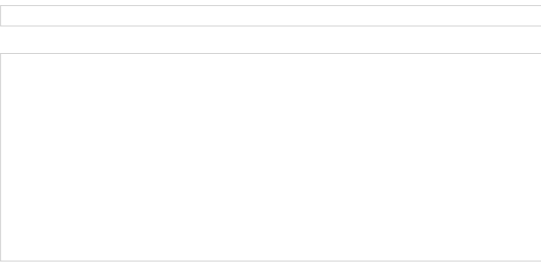
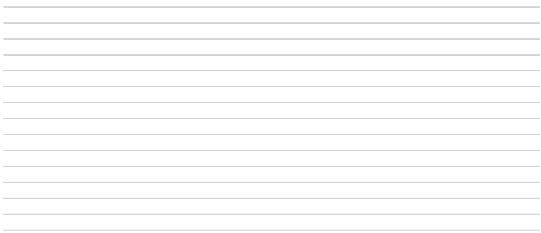
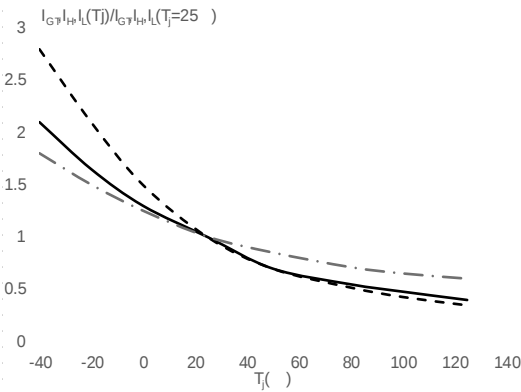


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature





PACKAGE MECHANICAL DATA

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