

JIEJIE MICROELECTRONICS CO., LTD.

TEL

Peak gate power	P_{GM}	5	W
Peak pulse voltage ($T_j=25$; non-repetitive,off-state;FIG.7)	V_{pp}	0.5	kV

NOTE 1: Operating junction temperature T_j is up to 125 when a resistor 1k is connected between Gate and Cathode. Without this resistor, the T_j is up to 110 only.

ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Value	Unit
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ORDERING INFORMATION

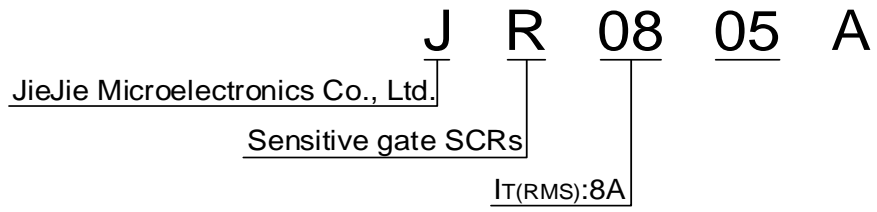


FIG.1: Maximum power dissipation versus RMS on-state current

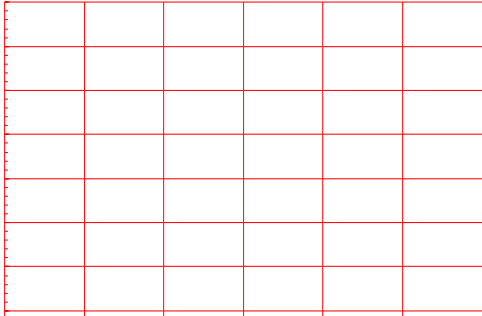
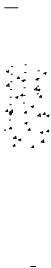
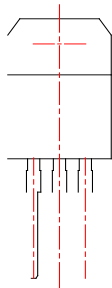


FIG.2: RMS on-state current versus case temperature

PACKAGE MECHANICAL DATA



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Pmj EMC /l(at)5 ()10 (rcua)10 (ng)1(p)10 ((t)2 (h)10 (n7 (at)2 (i)6 (6 (o)10 (n)10 (m)-3 (en)1n