

# JIEJIE MICROELECTRONICS CO., LTD.

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Peak pulse voltage ( $T_j=25$ ; non-repetitive,off-state;FIG.8)	$V_{pp}$	0.5	kV
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MARKING

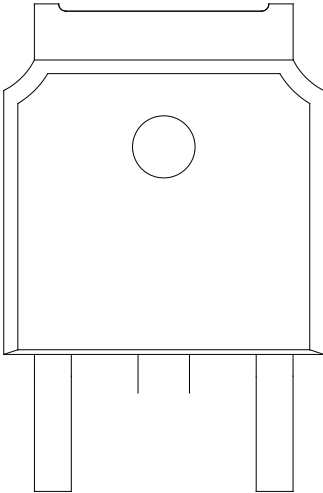


FIG.5: On-state characteristics

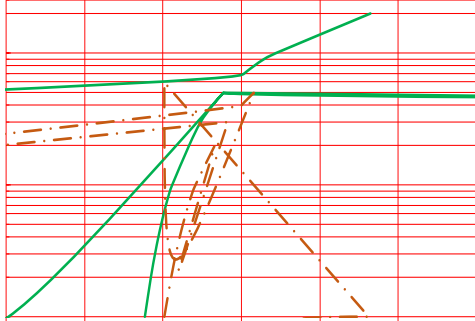


FIG.6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$ , and corresponding value of  $I^2t$  ( $dI/dt < 50\text{A}/\mu\text{s}$ )

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

Surge Gene





PACKAGE MECHANICAL DATA

Ref.	Dimensions			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
C	0.40		0.60	0.016		0.024
D						
E	6.40					
G						
G1	2.18		2.38	0.086		0.094
L						
				0.053	8 (-) 1.6 (4)	
6	-	0	8	6	8	d 0.40 0 M M

