	Power Dissipation Derating (T_a 25)	P_D/	-3.33	mW/

Total Power Dissipation

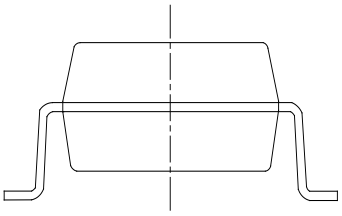
FIG.12: Test Circuits of Turn On Time

FIG.13: Waveforms of Turn On Time

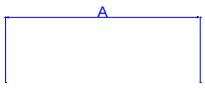
Fig.14: Test Circuits of dV/dt

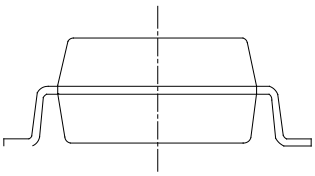
Fig.15: Waveforms of dV/dt

Option S Type:



Option SLM Type: DimensionsMillimetersInchesRef.Min.Typ.Max.Min.Typ.Max.





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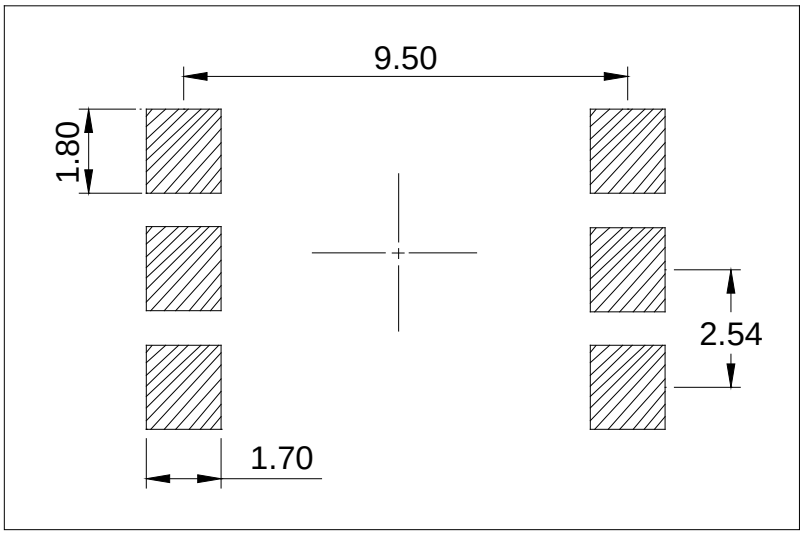

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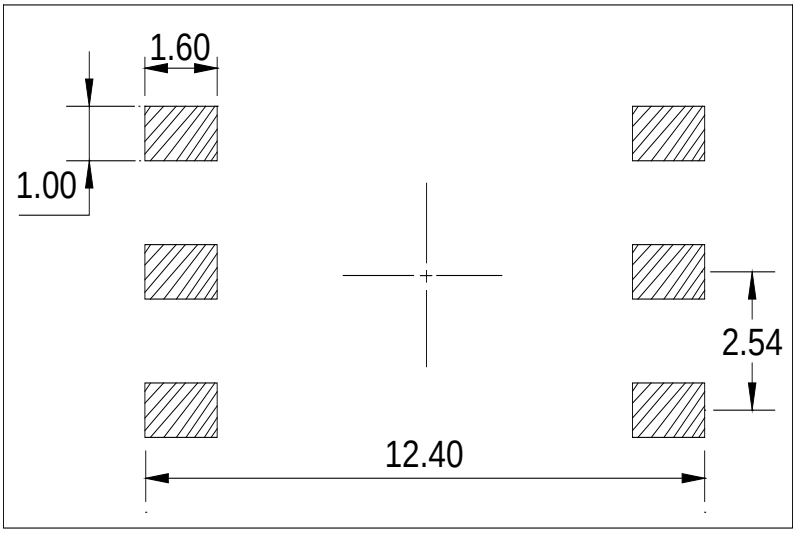
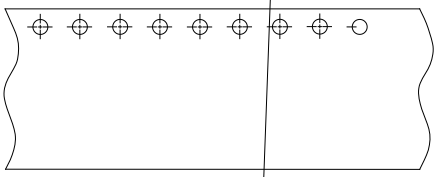
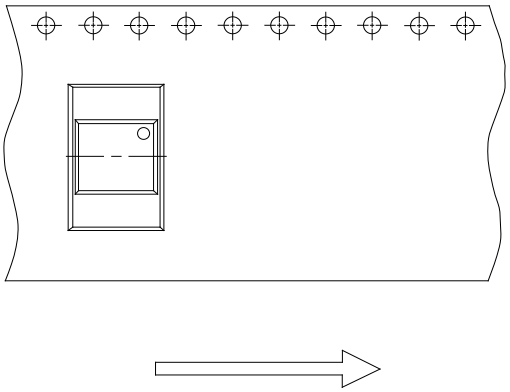
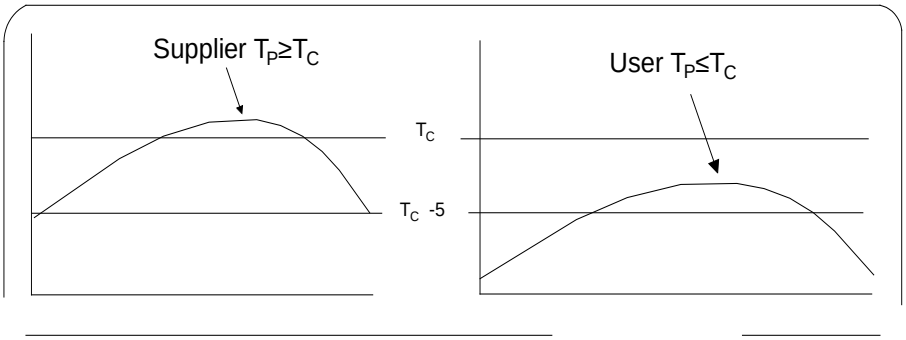
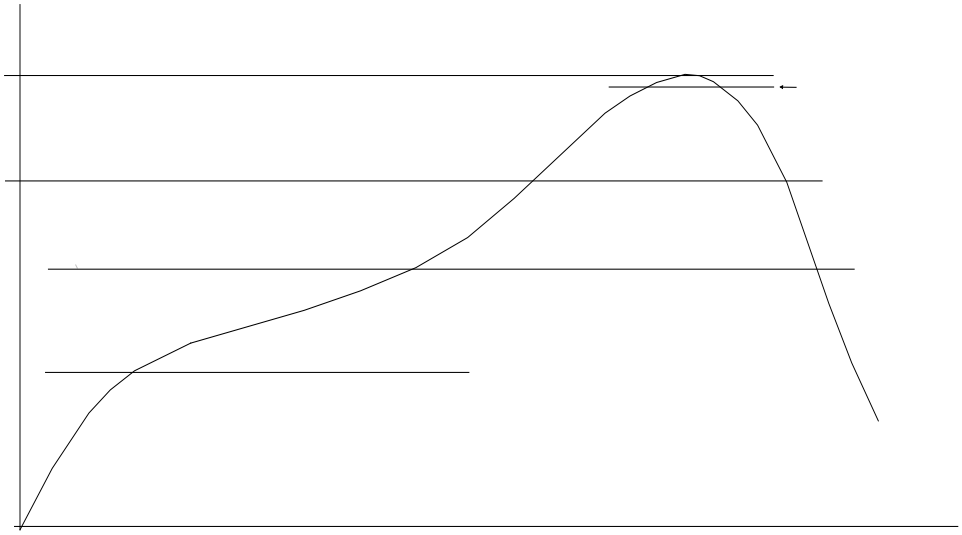


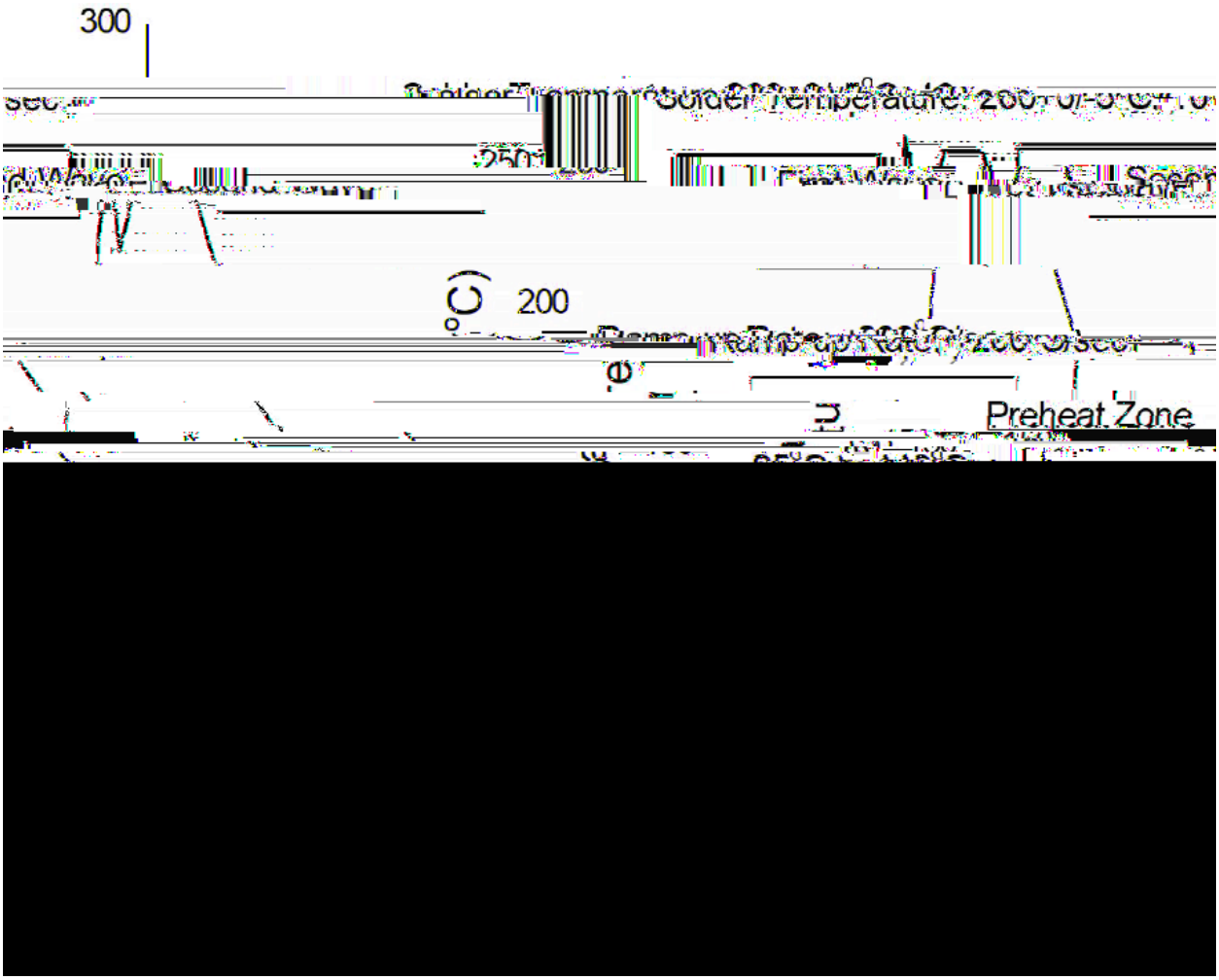
S






Supplier $T_P \geq T_C$
User $T_P \leq T_C$
T_C
T_C -5





Soldering Temperature	360± 5
Soldering Time	3s max.

Document Revision History

Date	Revision	Changes
Apr.2, 2025	A.1.0	Last update
Nov.5, 2025	A.1.1	Add S&SLM package

on pr doduc(i)6 ((f)2 (or)7 (m)-3 (a)10 (t)2 (i)6 2 (.)]TJ 0 Tc 0 5.24.07 0 Td ( )-c -0.004 Tc-0.002 T.19